

## ***AMENDMENT TO THE SPECIFICATION***

Please amend the paragraph starting on page 17, line 17 according to the following marked-up version. The Applicants hereby request to replace the original paragraph with this amended paragraph.

In general, a word voter of the present invention with  $n$  outputs for a triple modular redundancy simplex system includes three pair-wise matching circuits **840**, **850** and **860** each having  $n$  XNOR gates and  $n$  2-input AND gates (similar as to **FIG. 6**) to receive output vectors of modules (such as  $Z^1$  of the triple modular redundancy simplex system. A word voter simplex system further includes  $2n$  2-input AND gates **850** and  $n$  2-input OR gates **860** to generate said  $n$  outputs  $Z_1 \dots Z_n$ . Furthermore, the word voter for TMR simplex systems includes five two-input AND gates **850** and one two-input OR gate **860** independent of  $n$ . Lastly, the word voter for simplex systems includes three flip-flops **810**, **820** and **830** independent of said  $n$ . In order to generate error signal **840**, the word voter for ~~simplex~~ simplex systems includes a 1 3-input NOR gate **870** and which is independent of said  $n$ .